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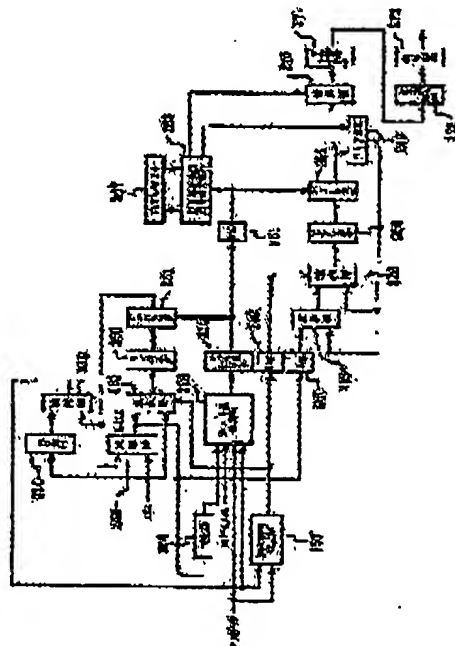
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## (54) DEVICE FOR ENCODING PICTURE AND DEVICE FOR DECODING PICTURE AND DEVICE FOR ENCODING AND DECODING PICTURE

## (57)Abstract:

PROBLEM TO BE SOLVED: To attain a high speed of an encoding processing by shifting data stored in a block register by plural bits at once based on a shift amount held in a shift amount register.

SOLUTION: A block register 250 stores the block data of plural bits used for encoding picture data. Arithmetic means 252, 254, 255, and 256 operates the update operation of the block register 250. A storing means stores the shift amounts shift-LS2 of the block register 250 when picture data are in a constant state. A shift amount deciding part 258 decides the shift amounts of the block register 250 based on the outputs of the arithmetic means 252, 254, 255, and 256 and the storing means. A shift amount register 259 holds the shift amounts calculated by the shift amount deciding part 258. A shift means 251 shifts data stored in the block register 250 by plural bits at once based on the shift amounts held in the shift amount register 259.



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